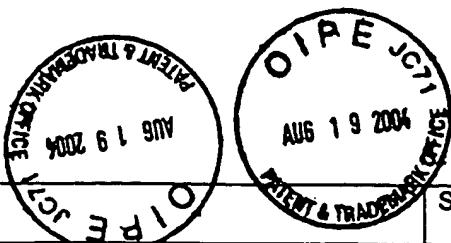


FORM PTO-1449	SERIAL NO. 10/774,758	CASE NO. 10519-114
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	FILING DATE February 9, 2004	GROUP ART UNIT 2818
APPLICANT(S): Crowley et al.		

1-2



REFERENCE DESIGNATION		U.S. PATENT DOCUMENTS			
EXAMINER INITIAL	DOCUMENT NUMBER Number-Kind Code (if known)	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
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CB	A2 6,420,215	7/16/2002	Knall et al.		
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EXAMINER INITIAL	DOCUMENT NUMBER Number-Kind Code (if known)	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO

EXAMINER INITIAL	OTHER ART - NON PATENT LITERATURE DOCUMENTS (Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.)	
CB	A22	"An Improved Method for Making High-Density Nonvolatile Memory," U.S. Patent Application Serial No. 10/326,470, Inventors: S. Brad Herner, Maitreyee Mahajani, and Michael A. Vyvoda, Filing Date: 12/19/2002.

EXAMINER	/Cynthia Britt/	DATE CONSIDERED	06/21/2006
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FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	SERIAL NO. 10/774,758 FILING DATE February 9, 2004 APPLICANT(S): Crowley et al.	CASE NO. 10519-114 GROUP ART UNIT 2818
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EXAMINER INITIAL	OTHER ART – NON PATENT LITERATURE DOCUMENTS <small>(Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.)</small>	
CB	A23	"Current Sensing Method and Apparatus Particularly Useful for a Memory Array of Cells Having Diode-Like Characteristics," U.S. Patent Application Serial No. 09/896,468, Inventor. Roy E. Scheuerlein, Filing Date: 6/29/2001.
CB	A24	"512Mb PROM with 8 Layers of Antifuse/Diode Cells," presented as ISSCC 2003/Session 16/Non-Volatile Memory/Paper 16.4 at the 2003 IEEE International Solid-State Circuits Conference (3 pages).

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